

8/26/03

PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

200208858-1

APPLICATION NO.

10/648,570

CONFIRMATION NO.

APPLICANT

Samuel D. Naffziger, et al.

FILING DATE

GROUP

2817

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
MBJ	1A	2003/0060176	03/27/2003	Heinonen, et al.	
MBJ	1B	6,157,247	12/05/2000	Abdesselem, et al.	
MBJ	1C	6,449,575 B2	09/10/2002	Bausch, et al.	
MBJ	1D	6,509,788 B2	01/21/2003	Naffziger, et al.	
	1E				
	1F				
	1G				
	1H				
	1I				
	1J				
	1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

MBJ	1Q	THOMAS D. BURD, et al., "A Dynamic Voltage Scaled Microprocessor System", IEEE Journal of Solid-State Circuits, Vol. 35, No. 11, pp. 1571-1580, November 2000
MBJ	1R	SHEKHAR BORKAR, et al., "Parameter Variations and Impact on Circuits and Microarchitecture", Circuit Research, Intel Labs, JF3-334, pp. 338-342, June 2003
	1S	

EXAMINER

SHINGLETON

DATE CONSIDERED

4/29/2005